Sona College of Technology, Salem-5

Department of Electronics and Communication Engineering

Date: 14.05.2019

Action taken report for stakeholders' recommendations.

S.No	Stakeholders	Recommendations	Action Taken
1.	Industry Experts	Mr.M.Kumar suggested to add Python Programming in first year new regulation 2019	Problem solving using Python Programming added in first year new regulation 2019
2.	Alumni	The course Basic Electrical and Electronics Engineering is needed for the first year	The course Basic Electrical and Electronics Engineering is retained
3.	Students	Students were asked to add the course python programming	The course python programming added in the regulation 2019
4.	Employers	The course Basic Electrical and Electronics Engineering lab is needed for the first year	The course Basic Electrical and Electronics Engineering lab added in regulation 2019
5.	Parents	Requested to provide latest technologies in the field of AI	The course AI is added as an elective course
6.	Academic Experts	Prof.N.S.Yoganathan suggested to change the course Electronic circuits and simulation lab experiments as per industry requirement	The course Electronic circuits and simulation lab experiments added as per industry requirement

Signature of Academic Coordinators

Prof.J.Harirajkumar Dr.S.Vijayalakshmi BOS Chairman

Dr.R.S.Sabeenian

Dr.R.S.SABEENIAN, M.E., MBA., Ph.D., FIETE, Professor and Head of Department Electronics and Communication Engineering SONA COLLEGE OF TECHNOLOGY, Salem -636 005. Tamilnadu, India.

Sona College of Technology, Salem-5

Department of Electronics and Communication Engineering

Date: 21.11.2019

Action taken report for stakeholders' recommendations.

S.No	Stakeholders	Recommendations	Action Taken
1.	Industry Experts	Verilog HDL code for Registers and counters can be added in the course Digital electronics	Verilog HDL code for Registers and counters added in combinational and sequential circuits
2.	Alumni	In the course Circuit Theory, DC theorems can be added	In Circuit Theory the DC theorems added in unit II
3.	Students	Students were asked to reduce the content of the course Circuit Theory	The content of the course Circuit Theory cannot be reduced as it is exam point of view only
4.	Employers	Requested to provide latest technologies in the field of ECE	The course Deep learning is added as an elective course
5.	Parents	Requested to provide latest technologies as per industry requirement	The course Machine learning added as an elective course
6.	Academic Experts	Prof. Vijaypeter suggested that the course Transforms and Differential Equations should have 3 lecture hour and 1 tutorial hour with 4 credits.	The course Transforms and Differential Equations have 3 lecture hour and 1 tutorial hour with 4 credits.

VEDIL Signature of Academic Coordinators

Prof.J.Harirajkumar

Dr.S.Vijayalakshmi

BOS Chairman

Dr.R.S.Sabeenian

Dr.R.S.SABEENIAN, M.E., MBA, Ph.D., FIETE, Professor and Head of Department **Electronics and Communication Engineering** SONA COLLEGE OF TECHNOLOGY, Salem - 636 005. Tamilnadu, India.